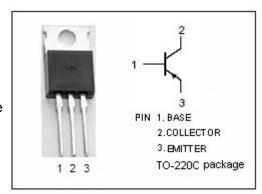


## **isc Silicon PNP Power Transistor**

# **CD551B**

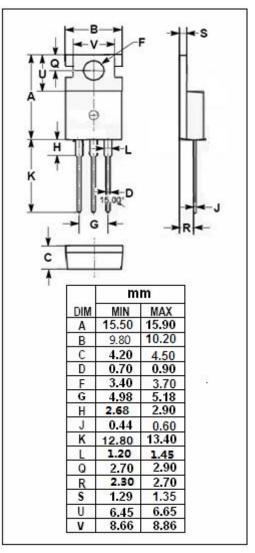
#### **DESCRIPTION**

 Minimum Lot-to-Lot variations for robust device performance and reliable operation



#### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT				
V <sub>CBO</sub>	Collector-Base Voltage	-35	V				
$V_{\text{CEO}}$	Collector-Emitter Voltage	-35	V				
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V				
Ic	Collector Current-Continuous	-3	Α				
Pc	Collector Power Dissipation @ T <sub>C</sub> =25℃	1.8	W				
TJ	Junction Temperature	125	°C				
T <sub>stg</sub>	Storage Temperature Range	-55~125	$^{\circ}\!\mathbb{C}$				





#### **isc Silicon PNP Power Transistor**

**CD551B** 

#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA; I <sub>B</sub> = 0	-35			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>E</sub> = 0	-35			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -0.1mA; I <sub>C</sub> = 0	-5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A; I <sub>B</sub> = -0.05A			-1.0	V
V <sub>BE(sat)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -0.05V			-1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -30V; I <sub>E</sub> = 0			-20	μА
I <sub>CEO</sub>	Emitter Cutoff Current	V <sub>CE</sub> = -30V; I <sub>C</sub> = 0			-100	μА
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -2V	55		270	

### **NOTICE:**

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