

isc Thyristors
C106D
FEATURES

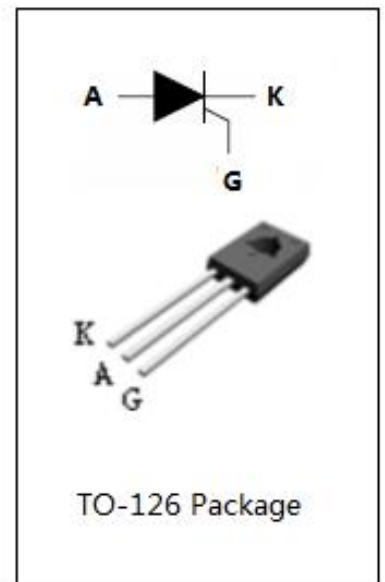
- Glassivated surface for reliability and uniformity
- Practical level triggering and holding characteristics
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high volume consumer applications such as temperature, light, and speed control; process and remote control, and warning systems where reliability of operation is important.

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	400	V
V _{RPM}	Repetitive peak reverse voltage	400	V
I _{T(AV)}	On-state current T _A =30°C	2.55	A
I _{T(RMS)}	RMS on-state current	4	A
I _{TM}	Surge peak on-state current	20	A
P _{GM}	Peak gate power	0.5	W
P _{G(AV)}	Average gate power	0.1	W
T _j	Operating Junction temperature	110	°C
T _{stg}	Storage temperature	-40 ~+150	°C
R _{th(j-c)}	Thermal resistance, junction to case	3	°C/W
R _{th(j-a)}	Thermal resistance, junction to ambient	75	°C/W


ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RPM} , V _{RM} =V _{RPM} , T _j =110°C			0.01 0.1	mA
I _{DRM}	Repetitive peak off-state current	V _{DM} =V _{DRM} , V _{DM} =V _{DRM} , T _j =110°C			0.01 0.1	mA
V _{TM}	On-state voltage	I _{TM} = 4A			2.2	V
I _{GT}	Gate-trigger current	V _{AK} =6V; R _L =100 Ω			200	μ A
V _{GT}	Gate-trigger voltage	V _{AK} =6V; R _L =100 Ω			0.8	V
I _H	Holding current	V _{AA} =12V; R _{GK} =1k Ω			3	mA

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