

isc Silicon NPN Power Transistor

2SD1730

DESCRIPTION

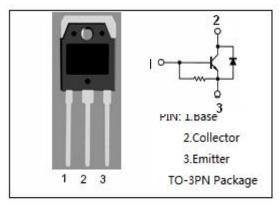
- · High Voltage
- · High Switching Speed
- Built-in damper diode
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

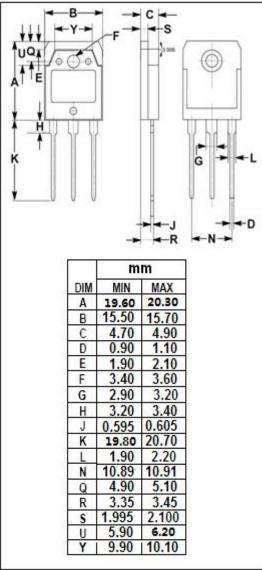
APPLICATIONS

• Designed for horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	1500	V	
Vces	Collector-Emitter Voltage	1500	V	
V _{CEO}	Collector-Emitter Voltage	700	V	
V _{EBO}	Emitter-Base Voltage	7	V	
Ic	Collector Current-Continuous	5	А	
I _{CP}	Collector Current-Peak		Α	
l _Β	Base Current- Continuous	2	Α	
Pc	Collector Power Dissipation @T _C =25 °C	100	W	
Tj	Junction Temperature	150	$^{\circ}\!\mathbb{C}$	
T _{stg}	Storage Temperature Range	-55-150	$^{\circ}$	







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT		
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E =200mA; I _C = 0	7			V		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 1A			8.0	V		
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 1A			1.5	V		
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	5		25			
Ісво	Collector Cutoff Current	V _{CB} = 750V; I _E = 0			10	μА		
		V _{CB} = 1500V; I _E = 0			1.0	mA		
V _{ECF}	C-E Diode Forward Voltage	I _F = 5A			2.3	V		
f⊤	Transition Frequency	I _C = 1A; V _{CE} = 10V		2		MHz		
Switching Times, Resistive Load								
ts	Storage Time	I _C = 4A; I _{B1} = 0.8A; I _{B2} = 1.6A,		1.5		μ S		
t _f	Fall Time	V _{CC} = 200V		0.2		μS		

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