

**isc Silicon NPN Power Transistor**
**2SC1368**
**DESCRIPTION**

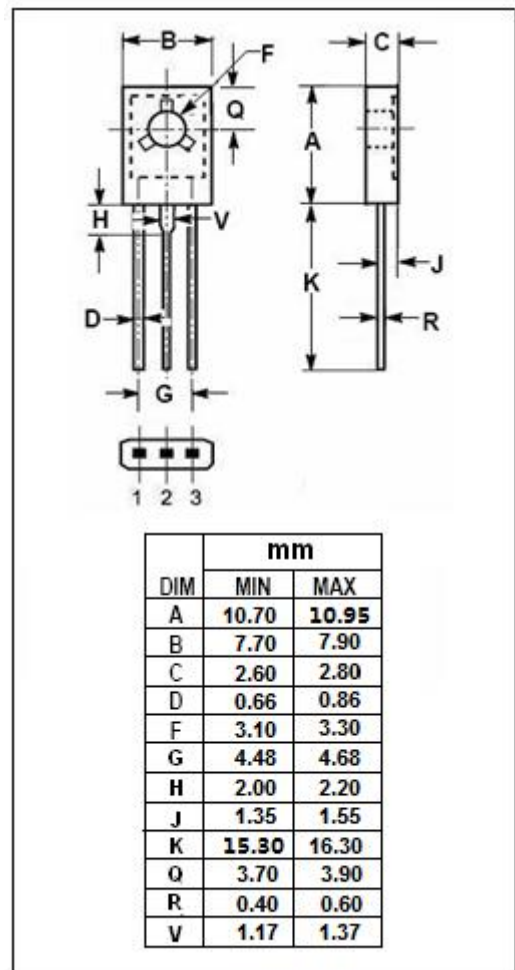
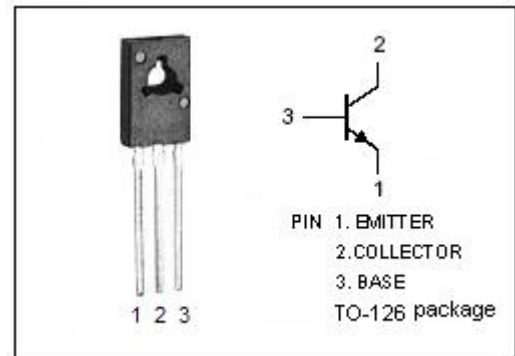
- High Collector Current  $I_C = 1.5A$
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = 25V(\text{Min})$
- Good Linearity of  $h_{FE}$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Designed for low frequency power amplifier applications.

**ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	25	V
$V_{CEO}$	Collector-Emitter Voltage	25	V
$V_{EBO}$	Emitter-Base Voltage	5	V
$I_C$	Collector Current-Continuous	1.5	A
$P_C$	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	8	W
	Collector Power Dissipation @ $T_a = 25^\circ\text{C}$	0.75	
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon NPN Power Transistor****2SC1368****ELECTRICAL CHARACTERISTICS****T<sub>C</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA ; I <sub>E</sub> = 0	25			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA ; R <sub>BE</sub> = ∞	25			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA ; I <sub>C</sub> = 0	5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1.5A ; I <sub>B</sub> = 0.15A			0.8	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 25V ; I <sub>E</sub> = 0			1.0	μ A
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 500mA ; V <sub>CE</sub> = 2V	60		320	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 500mA ; V <sub>CE</sub> = 5V		180		MHz

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