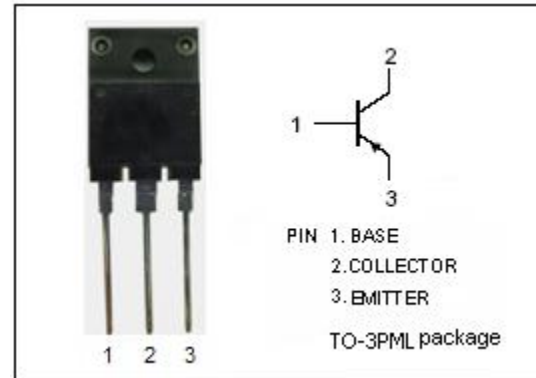


isc Silicon PNP Power Transistor
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DESCRIPTION

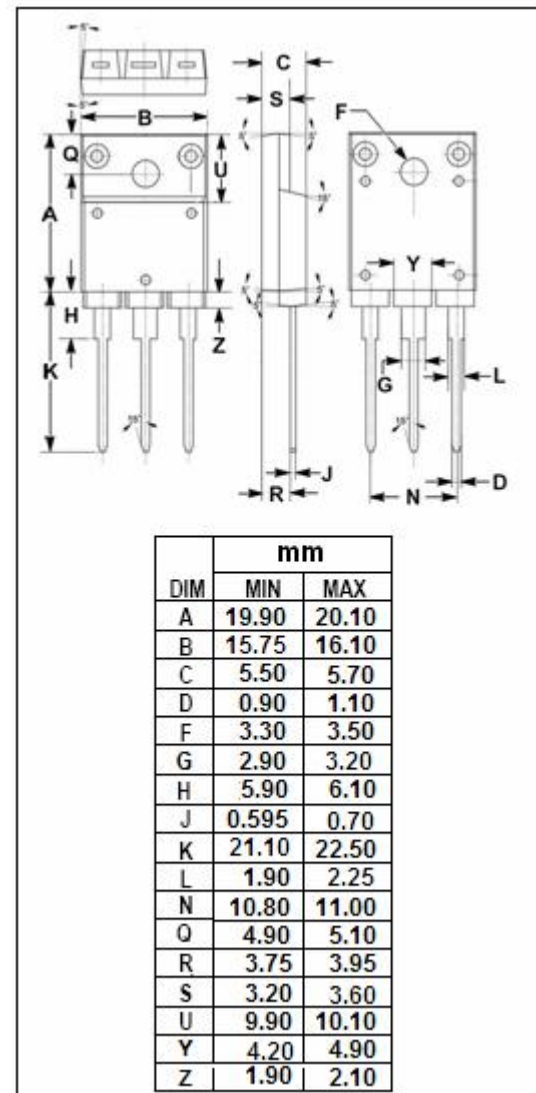
- Low Collector Saturation Voltage
: $V_{CE(sat)} = -0.5(V)(Max) @ I_C = -5A$
- Good Linearity of h_{FE}
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for chopper regulator, switch and general purpose applications


ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-70	V
V_{CEO}	Collector-Emitter Voltage	-50	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-12	A
I_{CM}	Collector Current-Peak	-20	A
I_B	Base Current-Continuous	-4	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	60	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



isc Silicon PNP Power Transistor

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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA ; I _B = 0	-50			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -80mA			-0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -80mA			-1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -70V ; I _E = 0			-10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -6V; I _C = 0			-10	μ A
h _{FE}	DC Current Gain	I _C = -5A ; V _{CE} = -1V	50			

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